Fig.1

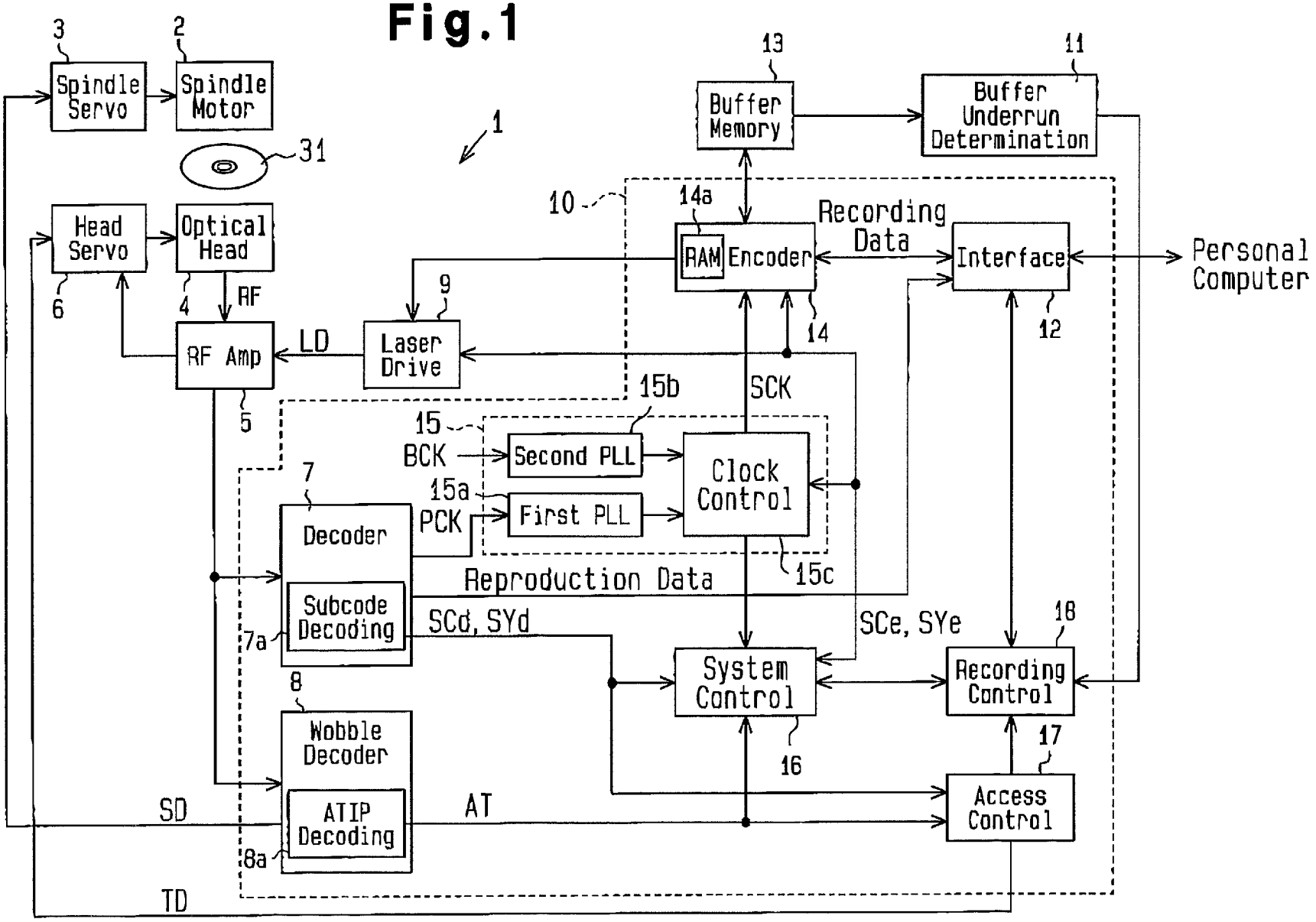
3
Spindle Servo
2
Spindle Motor
31
1
13
Buffer Memory
11
Buffer Underrun Determination
10
14a
RAM
Encoder
Recording Data
Interface
Personal Computer
Head Servo
Optical Head
6
4
RF
9
RF Amp
LD
Laser Drive
14
12
5
15b
SCK
15
BCK
Second PLL
Clock Control
7
15a
PCK
First PLL
Decoder
15c
Reproduction Data
Subcode Decoding
SCd, SYd
SCe, SYe
18
7a
System Control
Recording Control
8
Wobble Decoder
17
16
SD
ATIP Decoding
AT
Access Control
8a
TD

Fig.2(a)

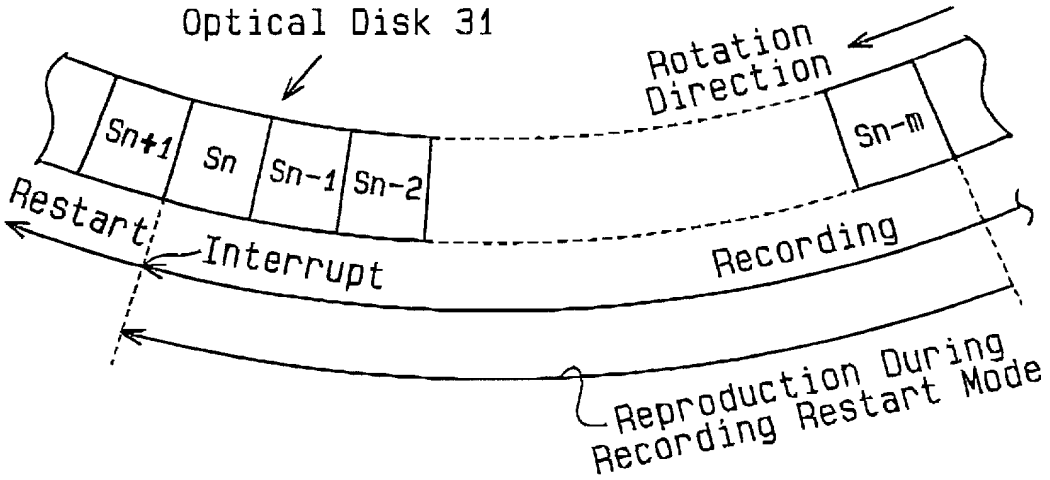

Fig.2(b)

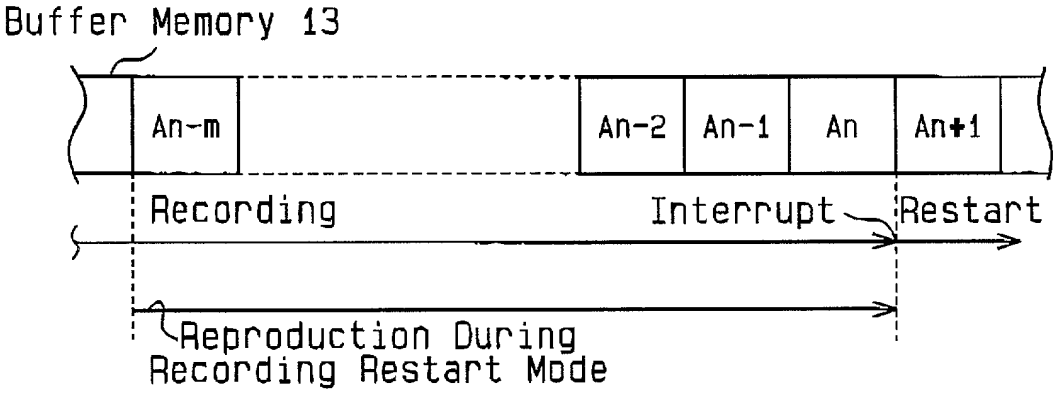

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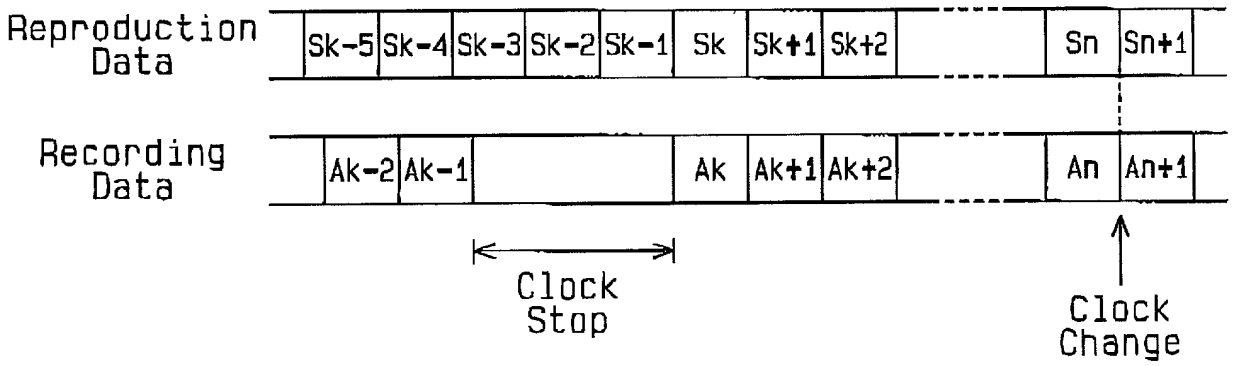
Fig.3



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